

# INTELLIGENT INTERACTIVE SYNTHESIZER

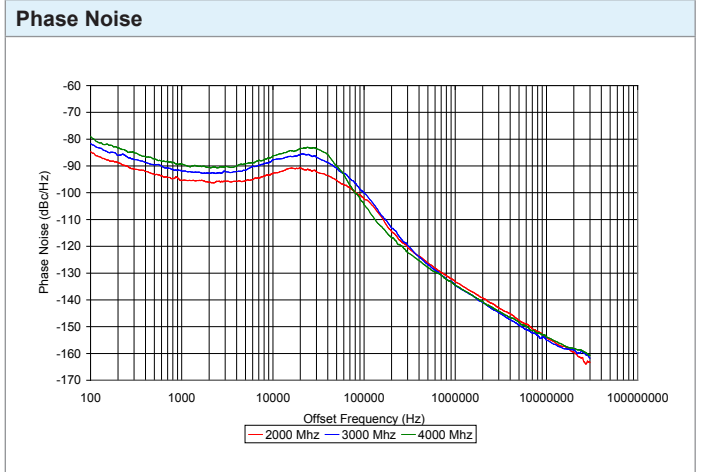
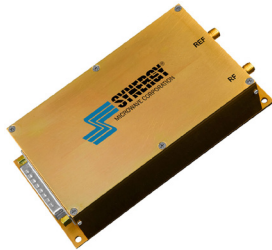
## CONNECTORIZED MODEL: KMTS2500-200400-20

WIDE BANDWIDTH

2000 - 4000 MHz

### FEATURES:

- ▶ Octave bandwidth tuning
- ▶ Small step size resolution
- ▶ Outstanding spectral purity
- ▶ High spurious rejection
- ▶ Fast lock settling time
- ▶ Easy programmable format
- ▶ Patented Rel-Pro® Technology
- ▶ RoHS Compliant
- ▶ Buffered Output



### SPECIFICATIONS

Output Frequency	2000 - 4000 MHz
Bandwidth	2000 MHz
External Reference <sup>1</sup>	20 MHz
External Reference Input	1 V p-p to 3.3 V p-p
Step Size	Programmable to 1 Hz
<b>Bias Voltage</b>	
VCC1	+12 VDC
VCC2	+12 VDC
VCC3	+12 VDC
<b>Bias Power Consumption</b>	
Bias Power Consumption	3 Watts (Max.)
Output Power	+5.5 dBm (Min.)
Spurious Suppression	50 dB (Typ.)
Harmonic Suppression	10 dB (Typ.)
<b>Patent : <a href="http://www.synergymw.com/patents">http://www.synergymw.com/patents</a></b>	

<b>Typical Phase Noise</b>	<b>Offset</b>	<b>dBc/Hz.</b>
	1 kHz	-88
	10 kHz	-87
	100 kHz	-100
<b>Settling Time <sup>2</sup></b>	Within 1 kHz	<10 mSec
	Within 10 Hz	<20 mSec
<b>Output Impedance</b>	50 Ohms (Nom.)	
<b>Operating Temperature Range</b>	-20 to +70 °C	
<b>Non-Operating Temperature Range</b>	-40 to +85 °C	
<b>Programming</b>	See Application Note: AN7400	
<b>Programming Bus <sup>3</sup></b>	Specify SPI or RS-232	

### Absolute Maximum Ratings

<b>Storage Temp. Range</b>	-55 to +125 °C
<b>Bias Voltage (+12 V)</b>	+15 V
<b>DC Voltage Applied to RF Out</b>	± 25 V

<sup>1</sup> Reference input frequency can be factory programmed for requested common reference frequencies from 10 - 25 MHz. Unless otherwise specified, the reference frequency is factory programmed for 20 MHz.

<sup>2</sup> Time between latch enable (LE) high (after user command) and final output frequency.

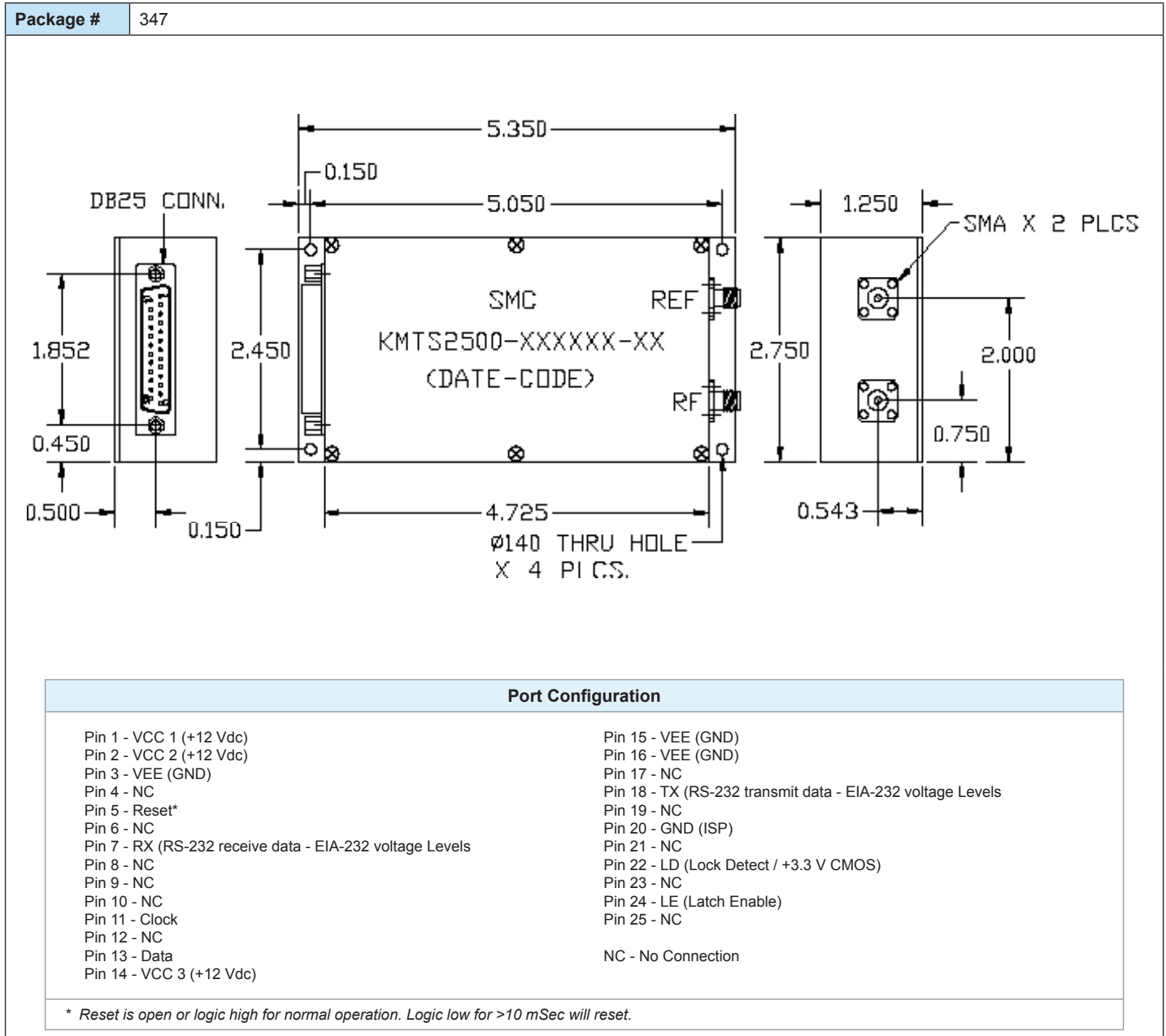
<sup>3</sup> Default bus (if unspecified) is SPI.

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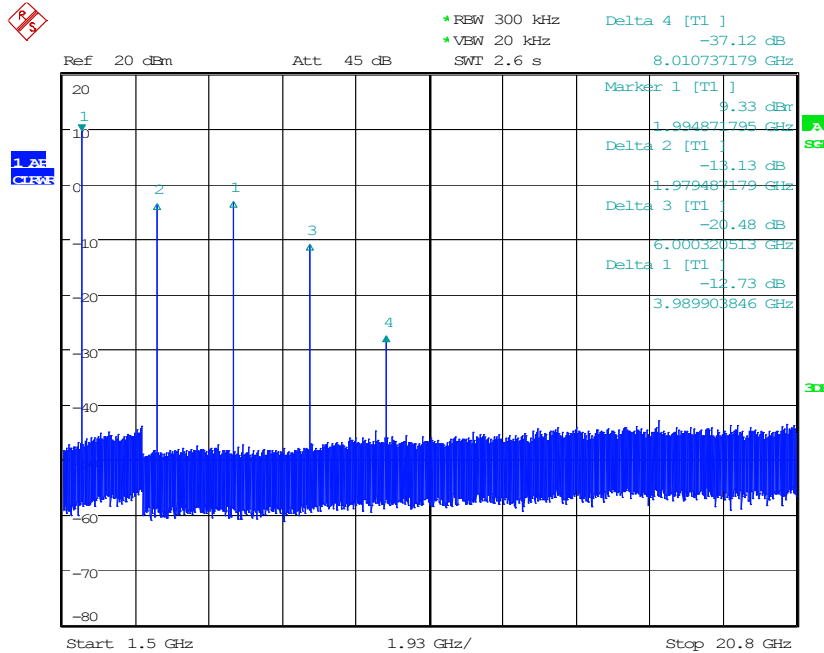


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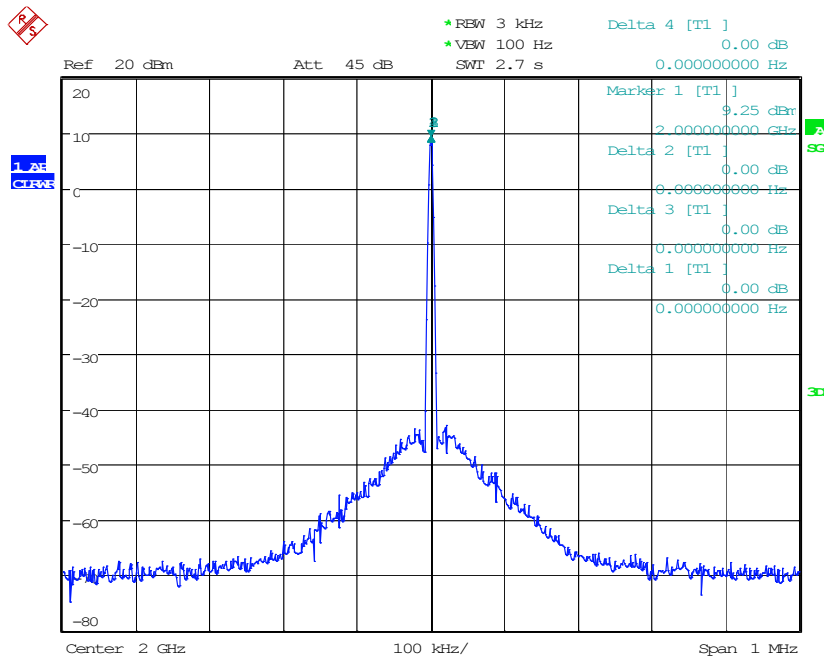
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HARMONIC SUPPRESSION



REFERENCE SIDEBAND SUPPRESSION



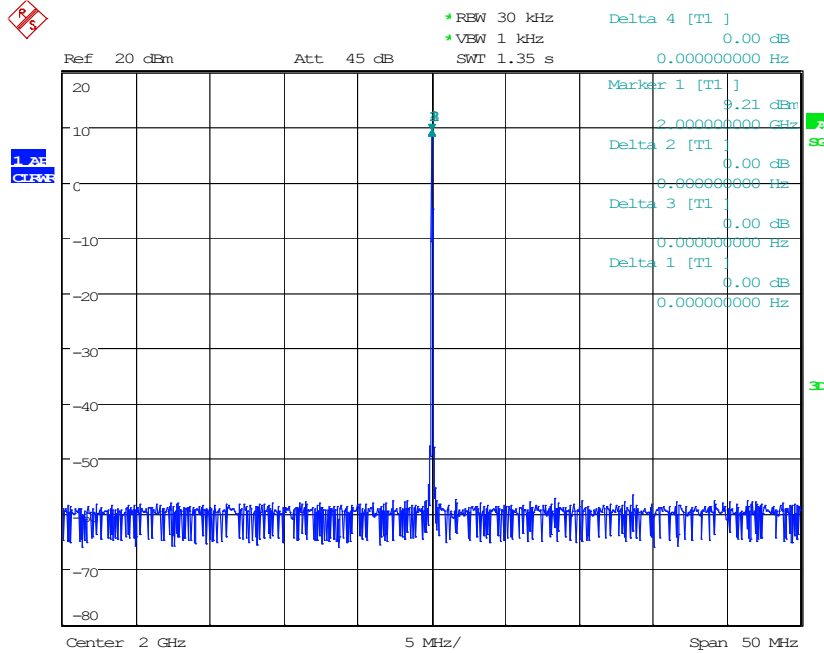
PERFORMANCE PLOTS

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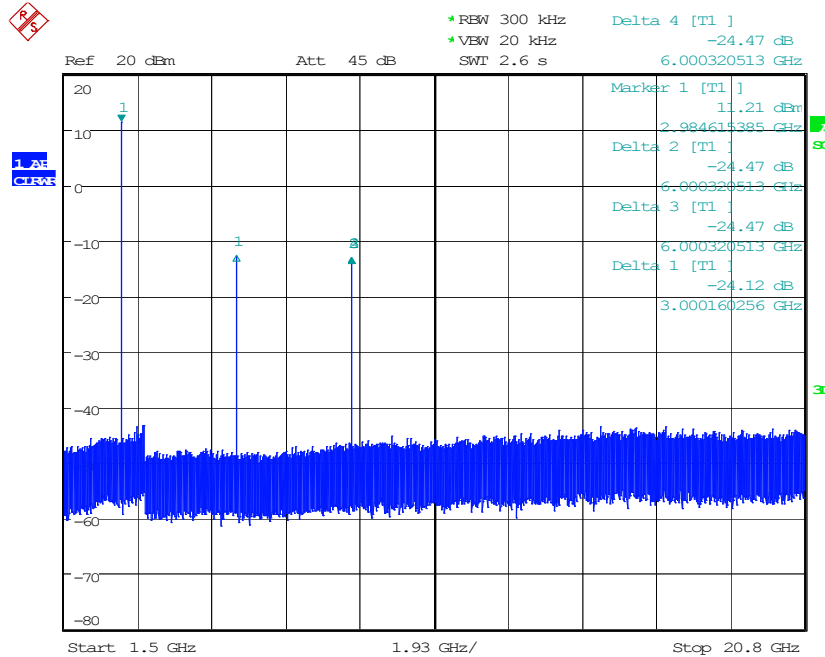
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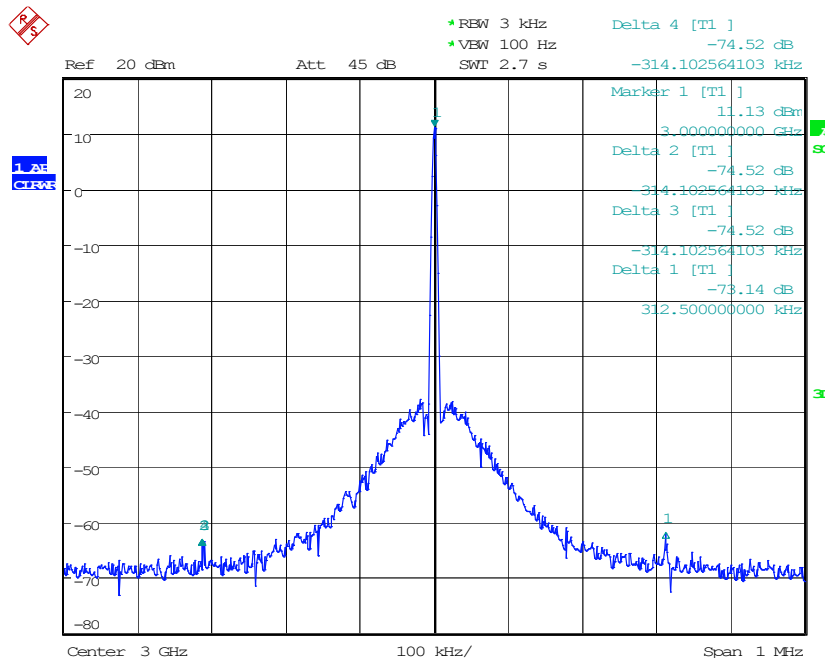
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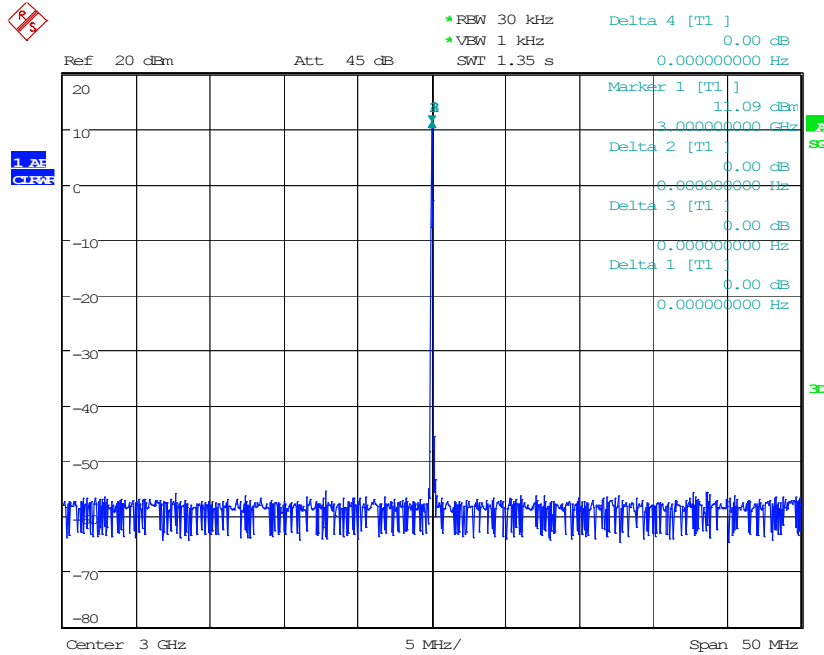
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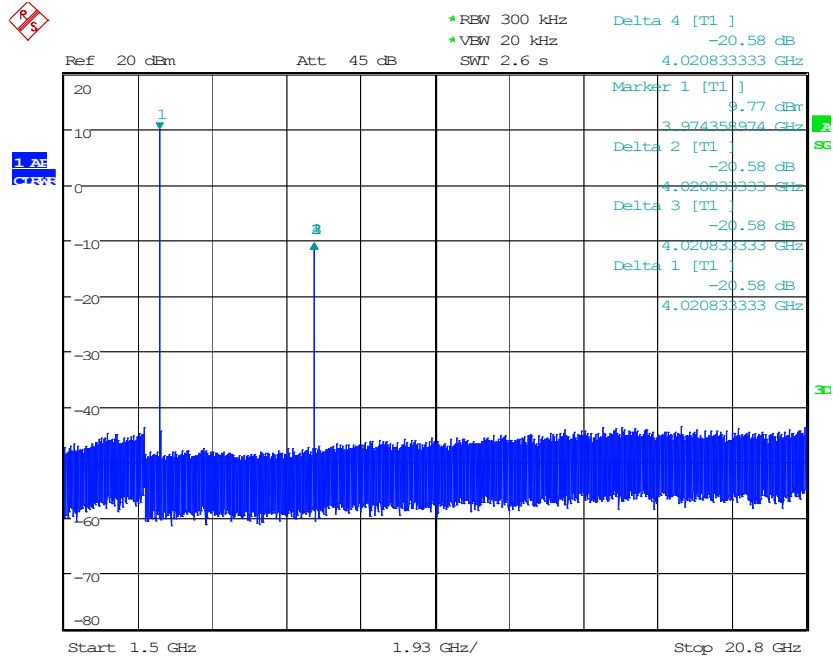
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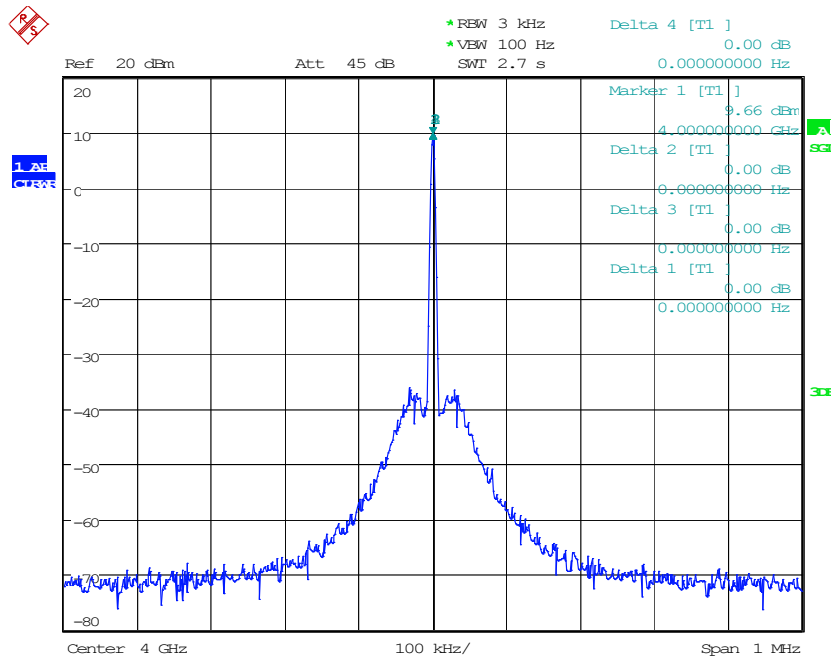
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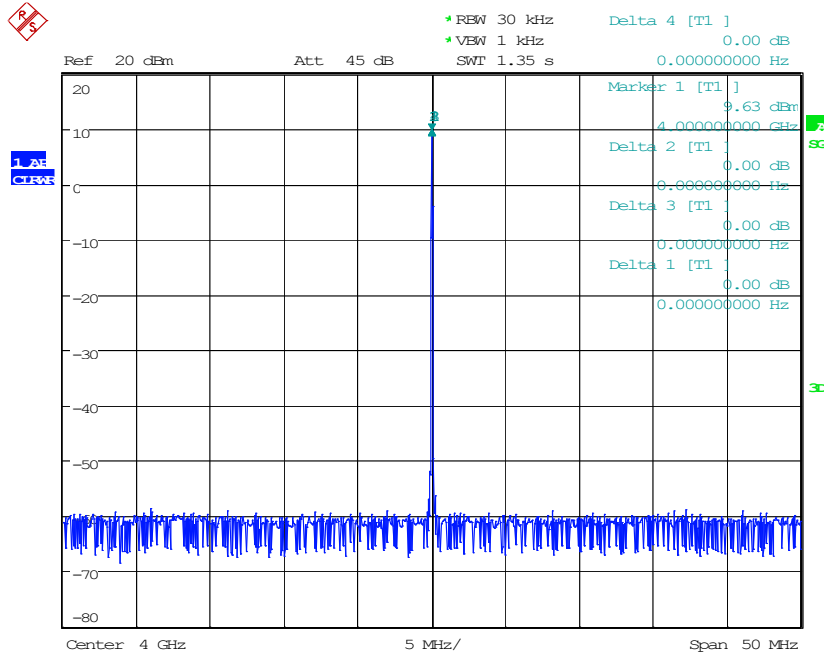
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